Advanced Formal Verification

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Advanced Formal Verification
Does RISC-V processor verification provide common ground to develop a new verification methodology, and will that naturally lead to new and potentially open tools?

New Methodologies Create New Opportunities
LONDON, April 06, 2021 (GLOBE NEWSWIRE) -- Axiomise, the leading provider of cutting-edge formal verification consulting, training, services, and IP, today unveiled a comprehensive introductory ...

Axiomise Unveils Formal Verification 101 Training Program
Peter Feist, OneSpin's president and chief executive officer, said, "Formal functional verification used to be a niche technology to increase the quality of a few critical components, and it required ...

OneSpin Enhanced 360 Module Verifier Delivers Industry's Firt Complete Multi-Configuration IP Verification Solution
Admix, the pioneering In-Play advertising platform that bridges the gap between mobile games and brands, announces a new partnership with Integral Ad Science (IAS), a global leader in digital ad ...

Admix In-Play Advertising Verified for the First Time by IAS
(Source: Cadence) Cadence's Paul Cunningham said, "Pre-silicon verification of advanced SoC design requires a solution ... Xcelium logic simulation, the JasperGold formal verification platform and the ...

Cadence speeds billion gate SoC verification
Detailed price information for Cadence Design Sys (CDNS-Q) from The Globe and Mail including charting and trades.

The Globe and Mail
Cadence and Arm are collaborating to speed SoC development using Cadence® tools and the new Arm® Neoverse V1 and Neoverse N2 platforms.

Cadence Collaborates with Arm to Accelerate Hyperscale Computing and 5G Communications SoC Development
While the resolution is a formal acknowledgement of the importance ... In contrast, navigational systems are perhaps among the most advanced networked and digitally accessible systems onboard.

Digital Perils: Socially Engineered Attacks in Maritime Cybersecurity
The Center for High Assurance Computer Systems (CHACS) performs fundamental research, exploratory development, and advanced technology demonstrations ... privacy and traffic engineering, formal ...

Center for High Assurance Computer Systems
He will bring his rich experience of computing and formal verification to provide strategic ... privacy preservation features, built on its advanced computation architecture and cryptography ...

Grigore Rosu Joins PlatON as Technical Advisor to Advance its Computing System Capacity
At last the United States could see into the interior of Soviet territory; limiting strategic arms became possible because for the first time the basic verification tools were available to both sides.

Spy Satellites and Other Intelligence Technologies that Changed History
Available in the U.S. for the first time, TripleCheck harnesses the power of three highly sophisticated verification methods to set a new ... While the technology behind TripleCheck is highly advanced ...

SmartSearch TripleCheck Sets New Standard in AML Security for U.S.
That means that it's an umbrella framework for all of the work that everyone else is doing, from work on synthesis and verification ... use of some of the more advanced functionality of the ...

Mithro Runs Down Open Source FPGA Toolchains
She's not alone. Terri Kirby also hasn't been able to verify her identity. She's so frustrated, she sent this formal complaint to the attorney general and she's pursuing a possible class action ...

ID.me Verification Leads To Problems For Some Coloradans Trying To File For Unemployment
"Pre-silicon verification of advanced SoC design requires a solution with multi-billion-gate capacity that offers both highest performance and rapid predictable debug," said Paul ...

Cadence Unveils Next-Generation Palladium Z2 and Protium X2 Systems to Dramatically Accelerate Pre-Silicon Hardware Debug and Software Validation
He will bring his rich experience of computing and formal verification to provide strategic suggestions ... network with first-class privacy preservation features, built on its advanced computation ...

Grigore Rosu Joins PlatON as Technical Advisor to Advance its Computing System Capacity
"Pre-silicon verification of advanced SoC design requires a solution with multi-billion-gate capacity that offers both highest performance and rapid predictable debug," said Paul Cunningham, senior ...